

ABSTRACT OF THE DISCLOSURE

In a communication control system for controlling packet transfer conducted by a plurality of logical channels between nodes, a circuit is provided for recording, in a descriptor for recording information regarding transfer by each logical channel, information including a stop bit, an activation bit, the number of logical channel to be activated, an identification value, the number of logical channel to be monitored and a monitoring identification value which are information regarding the order of transfer by each logical channel, and a data link layer includes a circuit for executing transfer by each logical channel based on the order of transfer by each logical channel designated by a descriptor.

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